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L1	312	(712/209).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/08/31 13:27
L2	576	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/08/31 11:21
L3	25	translat$4 near4 instruction$1 near4 templat$2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/08/31 11:27
L4	41	(translat$4 near4 instruction$1) with templat$2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/08/31 11:27
L5	1	("20020066004").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/08/31 13:27
S1	289	(712/209).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/07 14:25
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S3	24	java with (instruction near translat$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 17:10
S4	2	(("5307504") or ("5898885")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/07 14:25

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1 On the design of the local variable cache in a hardware translation-based java virtual ▨
     machine
     Hitoshi Oi
     June 2005 **ACM SIGPLAN Notices , Proceedings of the 2005 ACM SIGPLAN/SIGBED
                conference on Languages, compilers, and tools for embedded systems
                LCTES'05**, Volume 40 Issue 7
     Full text available: 📄 pdf(118.36 KB)   Additional Information: full citation, abstract, references, index terms

     Hardware bytecode translation is a technique to improve the performance of the Java
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     infeasible. However, since the translation is done on a single bytecode basis, it is likely to
     generate frequent memory accesses for local variables which can be a performance
     bottleneck.In this paper, we propose to add a small register file to the datapath of the
     hardware-translation based JVM and use it as a local variabl ...

     Keywords: hardware-translation, java virtual machine, memory hierarchy

2 Machine-adaptable dynamic binary translation   ▨
     David Ung, Cristina Cifuentes
     January 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN workshop on
                   Dynamic and adaptive compilation and optimization**, Volume 35 Issue 7
     Full text available: 📄 pdf(1.23 MB)   Additional Information: full citation, abstract, references, citings, index
                                                                     terms

     Dynamic binary translation is the process of translating and optimizing executable code for
     one machine to another at runtime, while the program is "executing" on the target
     machine.

     Dynamic translation techniques have normally been limited to two particular machines; a
     competitor's machine and the hardware manufacturer's machine. This research provides for
     a more general framework for dynamic translations, by providing a framework based on
     specifications of machines that ...

     Keywords: binary translation, dynamic compilation, dynamic execution, emulation,
     interpretation

3 Java bytecode to native code translation: the caffeine prototype and preliminary results ▨
     Cheng-Hsueh A. Hsieh, John C. Gyllenhaal, Wen-mei W. Hwu
     December 1996 **Proceedings of the 29th annual ACM/IEEE international symposium on
                    Microarchitecture**

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